

APPLICATION NO.

10/781,745

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02/20/2004

LEYKIN, RITA

ART UNIT PAPER NUMBER

EXAMINER

DATE MAILED: 05/16/2005

2837

Please find below and/or attached an Office communication concerning this application or proceeding.

FIRST NAMED INVENTOR

Yoshitaka Iwaji

				As
		Application No.	Applicant(s)	<u></u>
		10/781,745	IWAJI ET AL.	
	Office Action Summary	Examiner	Art Unit	
		Rita Leykin	2837	
Period fo	The MAILING DATE of this communication apports.	pears on the cover sheet v	vith the correspondence address	5
THE - Exte after - If the - If NO - Failt Any	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a reply period for reply is specified above, the maximum statutory period are to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailined patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, however, may a ly within the statutory minimum of th will apply and will expire SIX (6) MC e, cause the application to become a	a reply be timely filed irty (30) days will be considered timely. INTHS from the mailing date of this commun ABANDONED (35 U.S.C. § 133).	ication.
Status				
1)	Responsive to communication(s) filed on	_ ·		
2a) <u></u> □	This action is FINAL . 2b)⊠ This	s action is non-final.		
3)□	• • • • • • • • • • • • • • • • • • • •			
	closed in accordance with the practice under	Ex parte Quayle, 1935 C.	D. 11, 453 O.G. 213.	
Disposit	ion of Claims			
4)🖂	Claim(s) 1-11 is/are pending in the application	1.		
	4a) Of the above claim(s) is/are withdra	wn from consideration.		
5)⊠	Claim(s) 10 and 11 is/are allowed.			
·	Claim(s) 1 is/are rejected.			
	Claim(s) 2-9 is/are objected to.			
8)[_]	Claim(s) are subject to restriction and/o	or election requirement.		
Applicat	ion Papers			
•	The specification is objected to by the Examine			
10)	The drawing(s) filed on is/are: a) acc	cepted or b) objected to	by the Examiner.	
	Applicant may not request that any objection to the	drawing(s) be held in abeya	ance. See 37 CFR 1.85(a).	
_	Replacement drawing sheet(s) including the correct	·	- , , , ,	
11)[_	The oath or declaration is objected to by the E	xaminer. Note the attache	ed Office Action or form PTO-15	52.
Priority (under 35 U.S.C. § 119			
a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Burea See the attached detailed Office action for a list	ts have been received. ts have been received in ority documents have bee u (PCT Rule 17.2(a)).	Application No n received in this National Stag	e
		. S. the sertified copies file		
Attachmen 1) Notic	et(s) ce of References Cited (PTO-892)	4) Intendove	Summary (PTO-413)	
2) 🔲 Notic	ce of Draftsperson's Patent Drawing Review (PTO-948)	Paper No	(s)/Mail Date	
	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date 2/20/04.	5) Notice of 6) Other:	Informal Patent Application (PTO-152)	

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DETAILED ACTION

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 1. Claim 1 rejected under 35 U.S.C. 103(a) as being unpatentable over Ohmura et al. US # 6,556,458 and Sakai et al. US # 6,556,464.
- 2. With respect to claim 1, Ohmura et al. teach an inverter device with improved current detector. Wherein Fig. 2 shows:
 - A dc power supply 2 presented as diode bridge circuit 9 in coupling with power source 8;
 - A brushless motor 22:
 - An inverter, presented as inversion section 3;
 - A current detecting resistor 4 connected between the power source and inverter;
 - A PWM circuit 6g for controlling gate drive circuit output to motor;

Ohmura et al. do not teach sampling means. However, Sakai et al. disclose a PWM converter. Wherein in Fig. 1 Sakai et al. show:

A load 5, that is powered from AC power source 1a;

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 An inverter, presented as converter 3a, controlled via PWM gate signal generator 14a coupled to control power supply current command computation unit 12, wherein the output current of the later is sinusoidal, (see equation 1, column 4, lines 10-12);

- A current detecting means 7;
- A sampling and hold circuit which samples and holds a direct current, (see abstract).

Sakai et al. do not teach sampling means that is provided for sampling of the inverter current detected by current sensor connected between power supply and inverter. However, Sakai et al. employ in the system a sample-and-hold circuit, in connection with detector 9, that detects the input current waveform of the converter. Hence, it has been obvious to one of ordinary skills in the art, at the time invention was made to adjust the Sakai et al. system with sample and hold means to sample the current sensed by current detection means that is located between power source and inverter to control power supply of inverter, as it claimed.

The reason is to coordinate the supplied electric power to the load with motor current output.

Allowable Subject Matter

3. Claims 10 and 11 allowed.

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4. Claims 2-9 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

4. The following is a statement of reasons for the indication of allowable subject matter. The prior art made of record in the attached form PTO-892 considered to be pertinent to the submitted application.

However, none of the prior art teaches or suggest in combination:

 Sampling means that samples the inverter current in a time consisting of 33-67% of the power conduction time from a rise-up time of the inverter current.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rita Leykin whose telephone number is (571)272-2066. The examiner can normally be reached on Monday-Friday 8:30-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Martin can be reached on (571)272-2107. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Rita Leykin Primary Examiner Art Unit 2837

Rita Seyker

R.L.